	Туре	L#	Hits	Search Text	DBs	Time Stamp
1	BRS	L1		((fgpa pla pld asic (programmable adj logic adj (device array))) and column and row).clm.	US-PGPUB	2005/11/14 10:33
2	BRS	L2	72	1 and (region area block).clm.	US-PGPUB	2005/11/14 10:32
3	BRS	L3	50	1 and function.clm.	US-PGPUB	2005/11/14 10:32
4	BRS	L4	316	((fgpa pla pld asic (programmable adj logic adj (device array))) and (edge side)).clm.	US-PGPUB	2005/11/14 10:33
5	BRS	L5	39	4 and (row and column).clm.	US-PGPUB	2005/11/14 10:33